

Abstracts

A charge model of step recovery diode for CAD

K. Opalska and J. Baranowski. "A charge model of step recovery diode for CAD." 1997 MTT-S International Microwave Symposium Digest 3. (1997 Vol. III [MWSYM]): 1503-1506.

A physics-based, numerically efficient model of Step Recovery Diode (SRD) is presented. The model, based on partitioning of charge in the diode base into sections, has been implemented in generic circuit simulators and tested in simulations of variety of pulse circuits. It has been also verified by comparison of measurement data and model analysis results. The verification confirmed the model ability to represent accurately SRD pulse behaviour for a wide range of diode operating conditions.

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